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ABSTRACT OF THE DISCLOSURE

In a transducer, a sensor signal (2) is digitized and subsequently processed in an arithmetic-logic unit (7) into a setpoint value (A), which is finally converted into an analog output signal (I). The output signal (I) is detected, digitized and then supplied to the arithmetic-logic unit (7), which determines any deviation between the output signal (I) and the setpoint value (A). This makes it possible to determine and display the measuring error of the transducer in the conversion of the digital setpoint value (A) into the analog output signal (I) and, where necessary, to correct it in the arithmetic-logic unit (7).